Grain-size-dependent dielectric properties in nanograin ferroelectrics Ziming Cai, Xiaohui Wang State Key Laboratory of New Ceramics and Fine Processing, School of Materials Science and Engineering, Tsinghua University, Beijing 100084, PR China

Introduction: A series of ferroelectric ceramic models with grain and grain-boundary structures of different sizes are established via Voronoi tessellations. The relation between the electric displacement and electric field and the hysteresis loop are calculated using a finite element method based on a classical and modified hyperbolic tangent model via COMSOL Multiphysics.



(a)







Fig. 1. Schematic of (a) the loading setup for the computational domain in phase field model and (b) the polarization response of ferroelectric ceramic grain and grain boundary. The up-branch (red curve) represents the discharging process and the down-branch (black curve) represents the charge process.

Modified hyperbolic tangent model on electric hysteresis:

Fig. 3. Relative local electric field distribution of these ferroelectric ceramics at various grain size under a given applied electric field of 30 V/μm. Fig. 4. Dielectric permittivity as a function of electric field of the nanostructure ferroelectric ceramics.



1) Charge process (down branch)

$$P = P_{s} \tanh\left[\frac{\varepsilon_{0}(\varepsilon_{g}(0) - 1)E}{P_{s}}\right]$$
$$\varepsilon_{c}' = 1 + \frac{P_{s}}{\varepsilon_{0}E} \tanh\left[\frac{\varepsilon_{0}(\varepsilon_{g}(0) - 1)E}{P_{s}}\right]$$

2) Discharge process (up branch)

$$P = P_{s} \tanh\left[\frac{\varepsilon_{0}(\varepsilon_{g}(0) - 1)(E + E_{c})}{P_{s}}\right]$$
$$\hat{\varepsilon}_{c}' = 1 + \frac{1}{\varepsilon_{0}E}\left\{\hat{P}_{s} \tanh\left[\frac{\varepsilon_{0}(\varepsilon_{g}(0) - 1)(E + \hat{E}_{c})}{\hat{P}_{s}}\right] + \hat{P}_{s} - P_{s} - \hat{P}_{r}\right\}$$
O Gauss' law

 $\nabla \cdot D = 0.$

Results:

Fig. 5. (a) Electric displacement versus applied field of ferroelectric ceramics with different grain size up to their breakdown strength. (b) Energy discharge and charge density and efficiency against grain size at each breakdown point.

Fig. 6. (a) Electric displacement versus applied field of ferroelectric ceramics with different grain size up to 30 V/ μ m. (b) Energy discharge and charge density and efficiency against grain size both at the applied electric field of 30 V/ μ m.

Conclusion: The ferroelectric hysteresis loop are numerically calculated through a finite element method based on a classical and modified hyperbolic tangent model. It is found that as the grain size decreases, the dielectric permittivity is reduced. Under the same applied electric field, the ferroelectric ceramic with the smaller grain size possesses a lower discharge energy density but higher energy storage efficiency. When the applied electric field reaches their own breakdown strength, the smallest grain-sized ceramic displays the largest discharge energy density and energy storage efficiency. It is highly suggested that ferroelectric ceramics with smaller grain sizes can be used for applications in energy storage devices.



Fig. 2. Local electric field distribution of four different grain size ferroelectric ceramics: (a) 50 nm, (b) 75nm, (c) 100 nm, (d) 125 nm under the applied electric field of 30 V/ μ m.

References:

- 1. Gong H, Wang X, Zhang S, et al. Grain size effect on electrical and reliability characteristics of modified fine-grained BaTiO₃ ceramics for mlccs. J Eur Ceram Soc. 2014;34:1733-1739.
- 2. Huan Y, Wang X, Fang J, et al. Grain size effects on piezoelectric properties and domain structure of BaTiO₃ ceramics prepared by two-step sintering. J Am Ceram Soc. 2013;96:3369-3371.
- 3. Zheng P, Zhang JL, Tan YQ, et al. Grain-size effects on dielectric and piezoelectric properties of poled BaTiO₃ ceramics. Acta Mater. 2012;60:5022-5030.

Excerpt from the Proceedings of the 2018 COMSOL Conference in Shanghai